SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY
Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I FIRST SEMESTER VLSI AND EMBEDDED SYSTEMS AT REGULAR EXAM HELD DURING NOV/DEC 2023

						1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name			Course Code		EC609 T	EC611 T	EC613 T	EC615 P	EC623 T	EC617 T									
				Credit		**3.00**	**3.00**	**3.00**	**4.00**	**3.00**	**3.00**									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA		DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	HARDWARE DESCRIPTION LANGUAGE(ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 PRAKASH KUMAR RAJ						XX	XX	XX	XX	XX	NA									**BACKLOG : 19 CREDIT(S)**
P23VL001	19.00	0.00	0.00	0.00																

Total Student Count : 1

Abbrivations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10   AB-9   BB-8   BC-7   CC-6   CD-5   DD-4   FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFATORY ATTENDANCE NP/NF-AUDIT PASS/FAIL

The actual mark statement with the Academic / Examination section shall be final & binding to all

Date : 23-01-2024   Page 1 of 1