

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY
Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I FIRST SEMESTER VLSI AND EMBEDDED SYSTEMS AT REGULAR EXAM HELD DURING NOV/DEC 2022

					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC617 T									
					3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 ABHISHEK KUMAR					AA	AA	AB	AA	AA	AB									
P22VL001					19.00	19.00	9.68	9.68											
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC601 T	EC605 T									
					3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	RF CIRCUITS & SYSTEMS (ELECTIVE)	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)									
Semester : 1 VIJAY SAI VENKAT KASINA					AB	AB	AA	AA	AA	BB									
P22VL002					19.00	19.00	9.37	9.37											
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC617 T									
					3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 PRATEEK SINGH TOMAR					AA	AA	AB	AA	AB	AA									
P22VL003					19.00	19.00	9.68	9.68											
Semester : 1 SHANKHA SHUBHRA DATTA					AB	AA	AB	AA	AB	AB									
P22VL004					19.00	19.00	9.37	9.37											
Semester : 1 SUBHOJIT DEB					BB	AB	AB	AA	BB	BB									
P22VL005					19.00	19.00	8.74	8.74											
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC601 T	EC617 T									
					3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	RF CIRCUITS & SYSTEMS (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 RAJA BALWANTSINGH BISHT					AB	AA	AA	AA	AA	AA									
P22VL006					19.00	19.00	9.84	9.84											

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Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC617 T									
					3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 APOORVA AGRAWAL					BB	AB	BB	AB	BC	BB									
P22VL007 19.00 19.00 8.21 8.21																			
Semester : 1 SATYAM SINGH					BC	AB	AB	AB	BC	BB									
P22VL008 19.00 19.00 8.21 8.21																			
Semester : 1 RAMAKRUSHNA PADHY					BC	BB	AB	AA	CC	BC									
P22VL009 19.00 19.00 7.95 7.95																			
Semester : 1 RAMESH KUMAR					BB	BB	AA	AB	BB	BC									
P22VL010 19.00 19.00 8.37 8.37																			
Semester : 1 ANKIT KUMAR VERMA					BB	AB	AA	AB	BB	AB									
P22VL011 19.00 19.00 8.84 8.84																			
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC601 T	EC605 T									
					3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	RF CIRCUITS & SYSTEMS (ELECTIVE)	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)									
Semester : 1 NARENDRA YADALA					AB	AB	AB	AA	AA	BB									
P22VL012 19.00 19.00 9.21 9.21																			
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC601 T	EC617 T									
					3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	RF CIRCUITS & SYSTEMS (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 SHYAM SUNDAR					BB	BB	AA	AA	AA	AB									
P22VL013 19.00 19.00 9.21 9.21																			
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC617 T									
					3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 KOMAL KUMARI					BB	AA	AB	AB	BC	BB									
P22VL014 19.00 19.00 8.53 8.53																			
Semester : 1 RAJU SAH					BB	AB	BB	AB	BC	BC									
P22VL015 19.00 19.00 8.05 8.05																			

Abbreviations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFACTORY ATTENDANCE NP/NF-AUDIT PASS/FAIL

Date : 01-02-2023

The actual mark statement with the Academic / Examination section shall be final & binding to all

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Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC601 T	EC605 T									
		Credit			3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	RF CIRCUITS & SYSTEMS (ELECTIVE)	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)									
Semester : 1 PRASHANTH REDDY MADAPA					AB	BB	AB	AA	AA	BB									
P22VL016 19.00 19.00 9.05 9.05																			
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC601 T	EC617 T									
		Credit			3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	RF CIRCUITS & SYSTEMS (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 PREM KUMAR					AB	BC	AB	AA	AA	AB									
P22VL017 19.00 19.00 9.05 9.05																			
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC601 T	EC605 T									
		Credit			3.00	3.00	3.00	4.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	RF CIRCUITS & SYSTEMS (ELECTIVE)	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)									
Semester : 1 ASHOK MEKA					BB	AB	AA	AA	AA	BB									
P22VL018 19.00 19.00 9.21 9.21																			

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Semester	Student Name		Course Code		EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC617 T										
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	3.00	3.00	3.00	4.00	3.00	3.00										
					DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)										
Semester : 1 ANURAG PATIL					FF	CC	BC	BB	DD	FF										BACKLOG : 6 CREDIT(S)
P22VL019 19.00 13.00 4.37 6.38																				
Semester : 1 VILASH KRI					BC	BC	BB	BB	DD	BC										
P22VL020 19.00 19.00 6.89 6.89																				
Semester : 1 HRUSHIKESH SUNIL BADGUJAR					BB	BB	BB	AB	BB	BB										
P22VL021 19.00 19.00 8.21 8.21																				
Semester : 1 PIYUSH BUCCHA					AB	AA	AA	AA	BB	AA										
P22VL022 19.00 19.00 9.53 9.53																				
Semester : 1 PAWAN NATH GOSWAMI					BB	BB	AB	AB	BC	AB										
P22VL023 19.00 19.00 8.37 8.37																				
Semester : 1 RAJAN KUMAR VISHWAKARMA					BB	BB	AB	BB	BC	BC										
P22VL024 19.00 19.00 7.84 7.84																				
Semester : 1 ANANDI CHANDULAL PARGHI					BC	BB	BB	AB	CC	BC										
P22VL025 19.00 19.00 7.58 7.58																				

Total Student Count : 25