

## NEW PRODUCT/PROCESS DEVELOPMENT

- GDS of artifact suppression in ECG signal and Bio impedance demodulator have been submitted for the fabrication at SCL India.
- Research Project : Special Manpower Development Programme Chips to System Design (SMDP-C2SD)
- PI : Dr. Anand D Darji – Electronics & Communication Engineering Department
- Co-PI : Dr. Pinal J Engineer – Electronics & Communication Engineering Department
- Funding Agency : MeitY, Govt. of India

- FPGA based prototype design for noise cancellation system had been designed and developed which can be useful at LASER diagnostic LAB.
- Research Project : FPGA Based Adaptive Filter Algorithm Implementation for External Noise Cancellation
- PI : Dr. Anand D Darji – Electronics & Communication Engineering Department
- Co-PI : Dr. Jignesh N Sarvaiya – Electronics & Communication Engineering Department
- Funding Agency : BRNS

- Aerial System consists of sensors which helps in collecting the data as well as acts as the flying base station interfacing with various sensor motes, IoT devices and cloud for real time collaborative data processing
- Research Project : Collaborative Data Processing and Resource Optimization for Post Disaster Management and Surveillance using Internet of Things
- PI : Dr. Mukesh Zaveri – Computer Engineering Department
- Funding Agency : MeitY, Govt. Of India

- Developed SDR based ionospheric correction model for precise positioning of NavIC receiver
- Research Project : Study, Design and Simulation of Ionodelay effect based on Ionospheric Models and their correction techniques using Software Defined Radio
- PI : Dr. Shweta N Shah – Electronics & Communication Engineering Department
- Funding Agency : SAC, ISRO, Ahmedabad

