

R. Chudamani

Contact

Assistant Professor
Electrical Engineering Department
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Qualifications

B.E. (Electrical) 1990, SVRCET, Surat
M.Tech. (Power Electronics Electrical Machines and Drives) 1997, IIT Delhi
Ph.D. 2009, IIT Chennai

Area of interest

Active Power Filters, Interactive Grid, Electrical Drives

Teaching Experience

- Lecturer (Ad hoc) in Electrical Engineering Department, SVRCET, September-1991 to June-1996
- Lecturer in Electrical Engineering Department, SVNIT, January-1998, January 2006.
- Assistant Professor in Electrical Engineering Department, SVNIT, January-2006 to till date

Subjects taught

Electrotechniques, Electrical Machines, Electrical Networks, Power Electronics, Computer Applications to Electrical Engineering, Optimization Techniques (UG)

Ph.D. Thesis

Studies on Frequency and Harmonic Estimation for Shunt Active Power Filters.

Papers published in international journals

1. Power system frequency estimation method

R. Chudamani, Krishna Vasudevan and C.S. Ramalingam, "A Novel Power System Frequency Estimation Method," IET-Electronic Letters, Vol. 44, Issue-17, Aug. 14, 2008, pp.1030-1032.

2. Non-linear least-squares-based harmonic estimation algorithm for a shunt active power filter

R. Chudamani, Krishna Vasudevan and C.S. Ramalingam, "Nonlinear Least Squares based Harmonic Estimation for a Shunt Active Power Filter," IET-Power Electronics, Volume 2, issue 2, pp. 134-146.

3. Real-Time Estimation of Power System Frequency Using Nonlinear Least Squares

R. Chudamani, Krishna Vasudevan and C.S. Ramalingam, "Real Time Estimation of Power System Frequency using Nonlinear Least Squares," IEEE Transactions on

Power Delivery, Volume 24, Issue 3, July 2009, pp. 1021 – 1028.

Papers in international conferences

Nonlinear Least Squares Current Estimator for Three Phase Loads

1. R. Chudamani, Krishna Vasudevan and C.S. Ramalingam, "Nonlinear Least Squares Harmonic Current Estimator for Three phase Loads," IEEE International Conference on Industrial Technology, Dec. 17-19, 2006, pp. 1581-1586.

Simulation Study of a Shunt Active Power Filter Using Nonlinear Least Squares Harmonic Extraction Technique

2. R. Chudamani, Krishna Vasudevan and C.S. Ramalingam, "Simulation Study of Shunt Active Power Filters using Nonlinear Least Squares Harmonic Extraction Technique," IEEE international Conference of Power Electronics, Drives and Energy Systems for Industrial Growth, Dec. 12-17, 2006, pp. 1-5.

Comparative Evaluation of Harmonic Extraction Techniques for Three-Phase Three-Wire Active Power Filter

3. R. Chudamani, Krishna Vasudevan and C.S. Ramalingam, "Comparative Analysis of Harmonic Extraction Techniques for Three phase ThreeWire Shunt Active Power Filters," IEEE international Conference of Power Electronics and Drives, Nov.27-30, pp.1700-1705.

Short term Courses/Workshops Organized

- (i) Recent trends in Power Electronics
- (ii) Laboratory Curriculum Development in Electrical Engineering
- (iii) Microcontrollers and their Applications
- (iv) Power Electronics Education Workshop (NaMPET, CDAC)