

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
ELECTRICAL ENGINEERING DEPARTMENT

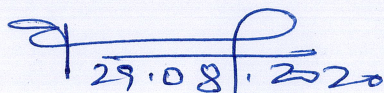
TIME TABLE OF M.TECH I (Power System Engineering)

SEMESTER – ODD (JULY-2020)

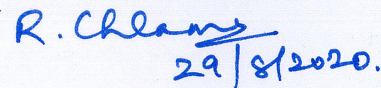
SESSION – 2020-21

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1	B1	C1	D1	LUNCH	P11		P12	
Tuesday	B2	C2	D2	E1		P13		P14	
Wednesday	C3	D3	E2	A2		P15		P16	
Thursday	D4	E3	A3	B3		P17		P18	
Friday	E4	A4	B4	C4		P19	H4	P20	

SL. NO	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASSROOM
1.	A1-A3	EL625	Restructuring of Power System	VM	PG	3	
2.	A4, C2	EL621	Power System Protection	HRJ	PG	4	
3.	B4, E2	EL621	Power System Protection	VAS	PG		
4.	P12	EL621	Power System Protection Lab	HRJ	PG	1	
5.	B1-B3	EL 631	Digital Signal Processing	PK	PG	3	
6.	D	EL 619	CA Power System Analysis	PD	PG	4	
7.	P14	EL619	CA PSA Lab	PBD	PG	1	
8.	C1, C3, H4	EL623	Power Electronics	MDG	PG	3	
9.	P18	EL623	Power Electronics Lab	MDG	PG	1	
10.	E1	EL629	Microcontroller Based System Design	VAS	Elective-I	3	
11.	E3, E4		Microcontroller Based System Design	JJP	Elective-I		


29.08.2020

(Chairman, Time Table Committee, EED)


29/8/2020.

(HoD, EED)