

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
ELECTRICAL ENGINEERING DEPARTMENT

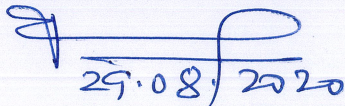
TIME TABLE OF M.TECH I (Power Electronics& Electrical Drives)

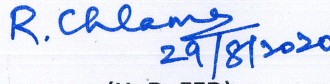
SEMESTER – ODD (JULY-DEC, 2020)

SESSION – 2020-21

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1	B1	C1	D1	LUNCH	P11		P12	
Tuesday	B2	C2	D2	E1		P13		P14	
Wednesday	C3	D3	E2	A2		P15		P16	
Thursday	D4	E3	A3	B3		P17		P18	
Friday	E4	A4	B4	C4		P19		P20	

SL. NO	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASS ROOM
1.	A1, A4	EL601	Power Electronics-I	RC	PG	4	
2.	C1, E2	EL601	Power Electronics-I	MAM	PG		
3.	P16	EL601	Power Electronics-I Lab	MAM,MDG	PG	1	
4.	B1-B3	EL 605	Digital Signal Processing	PK	PG	3	
5.	P20	EL605	Digital Signal Processing Lab	PK	PG	1	
6.	A2, B4	EL 613	Power Quality issues and Management	MA	Elective-I	2	
7.	P18	EL613	Power Quality Issues and Management Lab	MA		1	
8.	C2-C4	EL 607	Fundamentals of System Theory	RR	PG	3	
9.	D1, D2	EL603	Modelling of Electrical Machines & DC drives	AC	PG	4	
10.	D3, D4	EL603	Modelling of Electrical Machines & DC drives	SRA	PG		
11.	P12	EL603	Modelling of Electrical Machines & DC drives Lab	SRA	PG	1	
12.	E1	EL 617	Microcontroller Based System Design	VAS	Elective-I	2	
13.	E3, E4	EL 617	Microcontroller Based System Design	JJP			
14.	P14	EL617	Microcontroller Based System Design Lab	JJP		1	


 29.08.2020
 (Chairman, Time Table Committee, EED)


 29/8/2020
 (HoD, EED)