



Advertisement for the JRF position in the IDAPT-HUB, IIT BHU Sponsored Research project

Applications are invited from eligible candidates for the position of **Junior Research Fellow (JRF)** in a sponsored project funded by the **IDAPT-HUB, IIT BHU**, under the supervision of **Dr. Aeidapu Mahesh (PI), Dr. Naveen Yalla (IIT BHU) (Co-PI)**. The duration of the project is for maximum of two years. The selected candidate may have the opportunity to register in the Ph.D. program at SVNIT as per institute norms. The details of the project are mentioned below:

Title of the project	<i>Development of an Indigenous Web-Based Tool for Optimal Sizing and Integration of Hybrid Renewable Energy Systems</i>
Name & address of sponsoring agency	IDAPT-HUB, IIT BHU
Name of the post	Junior Research Fellow (JRF)
No. of post	01
Duration	One year, extendable up to two years or till project ends (whichever is earlier)
Investigator (s)	<ul style="list-style-type: none"><li>• <b>Dr. Aeidapu Mahesh</b>, Department of Electrical Engineering, Sardar Vallabhbhai National Institute of Technology Surat-395007 (Gujarat) (<b>PI</b>),</li><li>• <b>Dr. Naveen Yalla (Co-PI)</b>, Department of Electrical Engineering, Indian Institute of Technology, BHU, Varanasi, Uttar Pradesh (<b>Co-PI</b>).</li></ul>
Essential Qualification	<ul style="list-style-type: none"><li>• First Class M. E. /M.Tech. in Power Systems/Renewable Energy/Energy Science/Computer Science or Allied branches with B.E./B.Tech. in Electrical Engineering/Electrical and Electronics Engineering/Electronics and Communication Engineering/Computer Science Engineering/Data Science/Artificial Intelligence and other allied branches.</li><li>• Candidates should have qualified GATE (Graduate Aptitude Test in Engineering)</li></ul>
Desirable Qualification	<b>Strong proficiency in HTML, CSS, and JavaScript, Advanced Python skills, knowledge of meta-heuristic algorithms</b>
Terms and conditions	IDAPT HUB, IIT BHU and SVNIT Surat guidelines will be followed.
Fellowship	Rs. 37,000 /- + HRA (20%)
How to apply	A soft copy of the scanned application form (see Annexure-I) with all the supporting documents may be sent via email <a href="mailto:aeidapumahesh@eed.svnit.ac.in">aeidapumahesh@eed.svnit.ac.in</a> on or before <b>16-04-2026</b> . Original documents, including age proof, certificates, degrees, mark sheets, and other testimonials, must be presented at the time of the interview.
Important Dates	<ul style="list-style-type: none"><li>• Last Date to Apply (Online document): 16<sup>th</sup> April 2026, 05:00 PM.</li><li>• Display of eligible candidates list on Institute Website: 20<sup>th</sup> April, 2026</li><li>• Tentative date of interview/ test: 22<sup>nd</sup> – 29<sup>th</sup> April 2026</li></ul> <b>(Exact schedule will be communicated through email / institute website for shortlisted candidates only)</b>
Address of Correspondence	Dr. Aeidapu Mahesh, Assistant Professor, Department of Electrical Engineering, Sardar Vallabhbhai National Institute of Technology Surat Ichchhanath, Dumas Road - 395007, Gujarat, India Phone: +91 - 9034799994



**SARDAR VALLABHBHAI NATIONAL INSTITUTE OF  
TECHNOLOGY SURAT-395007**

सरदार वल्लभभाई राष्ट्रीय प्रौद्योगिकी संस्थान, सूरत -395007

**Department of Electrical Engineering**

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**Interested candidates are required to note the following:**

1. The interested candidates will be required to submit their application form (see Annexure - I and II) along with all essential details and scanned copy of all their original certificates, mark sheets and a recent passport size photograph by the last date, *i.e.*, 16/04/2026, 05:00 PM.
2. The mode of interview can be online/offline, which will be intimated to shortlisted candidates. The shortlisted Candidates need to bring the complete hardcopy set of the application form with xerox copies of all required documents on the day of interview, if called physically for verification purpose.
3. Candidates are required to positively mention their e-mail ID and Mobile Number in the Application Form.
4. The applications received within the last date & time stipulated will be screened by a selection committee, and a list of eligible candidates shortlisted for interview/test will be displayed on the Institute's official website along with the date and time schedule of the interview/test. The Screening Committee will devise its own criteria for shortlisting of candidates for interview. Merely fulfilling main eligibility criteria may not be suitable for eligible to be invited for the interview/test.
5. The decision of SVNIT-Surat in all matters relating to eligibility, acceptance, screening, mode of selection will be final and binding on the candidates and no enquiry correspondence will be entertained in this connection from any individual or any agency on behalf of the candidate.
6. The original documents of selected candidates will be verified at the time of Interview/joining, and in the event of any information mentioned in application/CV/testimonials found false or incorrect, their candidature straightway shall be cancelled.
7. No TA/DA will be paid for attending the interview/test.
8. For any queries regarding the position, please email to [aeidapumahesh@eed.svnit.ac.in](mailto:aeidapumahesh@eed.svnit.ac.in)

**Sd/-  
Director**



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**(Annexure-I)**

**Application for the JRF position in the IDAPT-HUB, IIT BHU Sponsored  
Research Project**

Paste recent  
passport size  
coloured  
photograph

1. Name of the candidate: \_\_\_\_\_
2. Father's/Husband's Name: \_\_\_\_\_
3. Gender: Male/Female: \_\_\_\_\_ Marital Status: \_\_\_\_\_
4. Whether belongs to SC/ST/PH/OBC/EWS: (If yes, attach certificate): \_\_\_\_\_
5. Address for Communication: \_\_\_\_\_  
\_\_\_\_\_ Pin: \_\_\_\_\_
6. E Mail: \_\_\_\_\_
7. Contact No.: \_\_\_\_\_
8. Permanent Address: \_\_\_\_\_  
\_\_\_\_\_ Pin: \_\_\_\_\_

9. Education Qualification: (Class X onwards)

Exam Passed	Board/University	% of Marks/ CPI/CGPA	Division	Year of Passing	Subjects

10. NET/GATE/ NBHM Examination passed:

Exam passed	Subject	Qualifying Date	Score/Category	Valid up to	Any other detail

11. Details of M. Tech. Dissertation:

Title:

Institute/University:

Name of Supervisor:

12. Knowledge of MATLAB: Yes/No

13. Knowledge of Meta-Heuristic optimization: Yes/No

14. Knowledge of Java script, HTML, CCS: Yes/No

15. Knowledge of Python programming: Yes/No

16. Details of employment (if any):

Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty	Any other detail

17. Any web project/software tool developed or any other relevant information that you may like to furnish:  
(attach separate sheet if required)

I declare that the above-given information is true to the best of my knowledge and belief. If any discrepancy is observed, my selection may be cancelled at any stage.

**Name and Signature of the Applicant**

For any queries regarding the position, please email [aeidapumahesh@eed.svnit.ac.in](mailto:aeidapumahesh@eed.svnit.ac.in)

A soft copy of the scanned application form with supporting documents may be sent via email to [aeidapumahesh@eed.svnit.ac.in](mailto:aeidapumahesh@eed.svnit.ac.in) on or before **16-04-2026**. Original documents, including age proof, certificates, degrees, mark sheets, and other testimonials, must be presented at the time of the interview.



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**Annexure II**

**Research Publications**

*(Name of the authors, Title of the paper, Journal/Conference name, volume, issue, Page Number)*