डिपार्टमेंट ऑफ़ इलेक्ट्रॉनिक्स इंजीनियरिंग
DEPARTMENT OF ELECTRONICS ENGINEERING
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ़ टेक्नोलॉजी, सूरत
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

क्रमांक: DECE/C2S/ /2025-26 **Date:** 01/04/2026

Advertisement for Recruitment of Research Personnel on Purely Contract Basis

Last date of Online application (April 14, 2026)

Applications are invited in prescribed format for the post of research personnel for Ministry of Electronics & Information Technology (MeitY), Government of India, New Delhi sponsored project entitled "*Secure and Energy Efficient Mixed Domain Compute in Memory-based AI Accelerator Chip for Edge Applications*" at the institute on purely contract basis initially for one year and extendible on yearly review-basis till the duration of the project i.e. 5 years.

The application form and the details of all educational qualifications and relevant experience required is available on Institute website *http://www.svnit.ac.in* and is also enclosed herewith.

Duly filled and signed application form along with self-attested scanned Ph. D. / M. Tech./ B.Tech. mark-sheets of all semesters, relevant experience certificates and necessary documents must be submitted in a single PDF file.

Link for submission of application form:
https://tinyurl.com/c2sprojectstaffsvnit-032026

Last Date to apply: On or before **April 14, 2026**

Applications received after said date will be not considered.

For any further information, please contact Dr. Pinalkumar Engineer (Chief Investigator), Associate Professor, (+91-99247-11335; Email: pje@eced.svnit.ac. in) Department of Electronics Engineering, Sardar Vallabhbhai National Institute of Technology, Surat, Gujarat, 395007

DIRECTOR

Eligibility Criterion:

For Sr. No. 1: As per OM DST/PCPM/Z-06/2022 dtd. 26/06/2023
For Sr. No. 2 and 3: as per DST OM SR/S9/Z-05/2019 dtd. 10/07/2020.

Sr. No	Post	Qualification	No. of. Posts / Duration	Salary
1.	Research Personnel (Junior Research Fellow)	B. E./ B.Tech./ M. E. / M. Tech. (Electronics/ Electronics & Communication/equivalent degree) **Qualified in National Level eligibility test CSIR-UGC NET/ GATE**	02 Posts/ 12 Months and extendible on yearly review-basis till the duration of the project.	Rs. 37,000/- + HRA@ 20% p.m.
	Desired Skills: 1. Digital / Analog /Mixed Mode Circuit Design, HDL Coding, 2. In-Memory computing 3. AI/ML and Processor architecture, 4. Python/MATLAB 5. Knowledge of OS (Windows / Linux / CentOS / RHEL), Server handling, network file system, knowledge of Cadence / Synopsys / Mentor Graphics EDA tools, etc.			
Note: The candidate shall note that this Advt. is for maximum 02 POSTs of Research personnel. The post will be filled for Research Personnel (JRF). However, in case of non-availability of suitable candidates for the post of Research Personnel (JRF), or in case Non receipt of any application meeting the qualification norms mentioned for JRF, the recruitment of Research Personnel (Project Associate-II/Project Associate-I) will be considered. The eligibility criterion for the said post of Research Personnel (Project Associate-II/Project Associate-I)) is mentioned below.				
2.	Research Personnel (Project Associate-II)	**B. E./ B.Tech.** (Electronics/ Electronics & Communication/ or equivalent) **And** 2 years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organizations and Scientific activities and services **Upper Age Limit:** 35 years	Maximum 02 Posts/ 12 Months and extendible on yearly review-basis till the duration of the project.	**With GATE/NET*:** Rs. 35,000/- + HRA@ 20% p.m. **Without GATE/NET*:** Rs. 28,000/- + HRA@ 20% p.m.
3.	Research Personnel (Project Associate-I)	**B. E./ B.Tech.** (Electronics/ Electronics & Communication/ or equivalent) **Upper Age Limit:** 35 years	Maximum 02 Posts/ 12 Months and extendible on yearly review-basis till the duration of the project.	**With GATE/NET*:** Rs. 31,000/- + HRA@ 20% p.m. **Without GATE/NET*:** Rs. 25,000/- + HRA@ 20% p.m.

How to apply:
The application form and the details of all educational qualifications and relevant experience required for this position is available on Institute website http://www.svnit.ac.in**.** Duly filled and signed application form along with self-attested scanned copies of B. E. /B. Tech/ M. E. /M. Tech. / M. Sc. / Ph. D. mark-sheets of all semesters, relevant experience certificates and other necessary documents if any must be submitted as a single PDF file by email at pje@eced.svnit.ac.in on or before **April 14, 2026**. Applications received after said date will be not considered. *Kindly also bring relevant documents of the same (Original and Xerox) at the time of the interview.*

Please Note:

1. Candidates who got selected may be allowed to enroll for M. Tech. / Ph.D. program subject to the fulfillment of eligibility conditions of SVNIT, Surat.
2. Candidates having minimum aggregate First Class/CGPA 6.5 for all qualified degrees and B.E. /B. Tech. /M.E. /M. Tech./PhD can only apply. The candidates with relevant experience will be given preference.
3. The names of eligible candidates will be uploaded on the institute website on **April 15, 2026**.
4. Online Interview for the shortlisted candidates will be held on **April 17, 2026**. However, final date/venue of Interview/Tests will be communicated.
5. An applicant must ensure the authenticity of information provided in support of experience claimed, other documents and photograph.
6. No TA/DA will be paid for appearing in the interview and/or joining the position.
7. Candidate employed in institute/Industry must produce No-Objection Certificate (NOC) at the time of interview.
8. List of selected candidates will be displayed on the institute website within one week after interview held

फ़ोन नं: संस्थान कार्यालय: २२२३३७१-७४, फैक्स नं:२२२८३९४,२२२७३३४ विभागीय प्रमुख: २२०१५५१, विभाग कार्यालय: २२०१५५२
ई-मैल: director@svnit.ac.in, hod@eced.svnit.ac.in