



**SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY**  
**SURAT-395007**

**सरदार वल्लभभाई राष्ट्रीय प्रौद्योगिकी संस्थान, सूरत -395007**  
**Department of Electrical Engineering**

**Advertisement for the JRF position in the ANRF Sponsored MAHA EV-Mission Project**

Applications are invited from eligible candidates for the position of **Junior Research Fellow (JRF) (2 Nos.)** in a sponsored MAHA-EV Mission project funded by the **Anusandhan National Research Foundation (ANRF), Ministry of Science and Technology, Government of India**, under the supervision of **Dr. Mahmdasraf A. Mulla (PI), Dr. Aeidapu Mahesh (PI), Dr. Suresh Lakhimsetty (PI)**. The duration of the project is for maximum of three years. The selected candidate may have the opportunity to register in the Ph.D. program as per institute norms. The details of the project are mentioned below:

Title of the project	<b>Empowering Research in Indigenous Development of EV Sub-systems (E-RIDES)</b>
Name & address of sponsoring agency	Anusandhan National Research Foundation (ANRF), Ministry of Science and Technology, Government of India.
Name of the post	JRF
No. of post	02
Duration	One year, extendable up to three years or till project ends (whichever is earlier)
Investigator (s)	<ul style="list-style-type: none"><li>• <b>Dr. Mahmdasraf A. Mulla</b>, Department of Electrical Engineering, Sardar Vallabhbhai National Institute of Technology Surat-395007 (Gujarat) <b>(PI)</b>,</li><li>• <b>Dr. Aeidapu Mahesh</b>, Department of Electrical Engineering, Sardar Vallabhbhai National Institute of Technology Surat-395007 (Gujarat) <b>(PI)</b>,</li><li>• <b>Dr. Suresh Lakhimsetty</b>, Department of Electrical Engineering, Sardar Vallabhbhai National Institute of Technology Surat-395007 (Gujarat) <b>(PI)</b>.</li></ul>
Essential Qualification	<ul style="list-style-type: none"><li>• First Class in M.E. /M.Tech. in Power Electronics, Power Electronics and Drives, Electrical Engineering, or related Allied branches.</li><li>• Candidate should have qualified GATE or NET.</li></ul>
Desirable Qualification	<b>JRF1:</b> Background in Power Electronics & Drives, Programming with microcontrollers, Speed control techniques, PCB design, MATLAB simulations. <b>JRF2:</b> Battery Management Systems, expertise on applying Deep Learning Algorithms for various applications, Python programming, Embedded programming, PCB design.
Terms and conditions	ANRF and SVNIT Surat guidelines will be followed.
Fellowship	Rs. 37,000 /- + HRA (20%)
How to apply	A soft copy of the scanned application form (see Annexure-I) with all the supporting documents may be sent via email <a href="mailto:aeidapumahesh@eed.svnit.ac.in">aeidapumahesh@eed.svnit.ac.in</a> on or before <b>10-09-2025</b> . Original documents, including age proof, certificates, degrees, mark sheets, and other testimonials, must be presented at the time of the interview.
Important Dates	<ul style="list-style-type: none"><li>• Last Date to Apply (Online document): 10<sup>th</sup> September 2025, 05:00 PM.</li><li>• Display of eligible candidates list on Institute Website: 12<sup>th</sup> September 2025</li><li>• Tentative date of interview/ test: 22<sup>nd</sup> – 26<sup>th</sup> September 2025</li></ul> <b>(Exact schedule will be communicated through email / institute website for shortlisted candidates only)</b>
Address of Correspondence	Dr. Aeidapu Mahesh, Assistant Professor, Department of Electrical Engineering, Sardar Vallabhbhai National Institute of Technology Surat Ichchhanath, Dumas Road - 395007, Gujarat, India Phone: +91 - 9034799994



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**Interested candidates are required to note the following:**

1. The interested candidates will be required to submit their application form (see Annexure - I and II) along with all essential details and scanned copy of all their original certificates, mark sheets and a recent passport size photograph by the last date, *i.e.*, 10/09/2025, 05:00 PM.
2. Candidates need to bring the complete hardcopy set of the application form with xerox copies of all required documents on the day of interview for verification purpose.
3. Candidates are required to positively mention their e-mail ID and Mobile Number in the Application Form.
4. The applications received within the last date & time stipulated will be screened by a selection committee, and a list of eligible candidates shortlisted for interview/test will be displayed on the Institute's official website along with the date and time schedule of the interview/test. The Screening Committee will devise its own criteria for shortlisting of candidates for interview. Merely fulfilling main eligibility criteria may not be suitable for eligible to be invited for the interview/test.
5. The decision of SVNIT-Surat in all matters relating to eligibility, acceptance, screening, mode of selection will be final and binding on the candidates and no enquiry correspondence will be entertained in this connection from any individual or any agency on behalf of the candidate.
6. The original documents of selected candidates will be verified at the time of Interview, and in the event of any information mentioned in application/CV/testimonials found false or incorrect, their candidature straightway shall be cancelled.
7. No TA/DA will be paid for attending the interview/test.
8. For any queries regarding the position, please email to [acidapumahesh@eed.svnit.ac.in](mailto:acidapumahesh@eed.svnit.ac.in)

**Sd/-  
Director**



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**(Annexure-I)**

**Application for the JRF position in the ANRF Sponsored  
MAHA-EV Mission Project**

Paste recent  
passport size  
coloured  
photograph

1. Name of the candidate: \_\_\_\_\_
2. Father's/Husband's Name: \_\_\_\_\_
3. Gender: Male/Female: \_\_\_\_\_ Marital Status: \_\_\_\_\_
4. Whether belongs to SC/ST/PH/OBC/EWS: (If yes, attach certificate): \_\_\_\_\_
5. Address for Communication: \_\_\_\_\_  
\_\_\_\_\_ Pin: \_\_\_\_\_
6. E Mail: \_\_\_\_\_
7. Contact No.: \_\_\_\_\_
8. Permanent Address: \_\_\_\_\_  
\_\_\_\_\_ Pin: \_\_\_\_\_

9. Education Qualification: (Class X onwards)

Exam Passed	Board/University	% of Marks/ CPI/CGPA	Division	Year of Passing	Subjects

10. NET/GATE/ NBHM Examination passed:

Exam passed	Subject	Qualifying Date	Score/Category	Valid up to	Any other detail

11. Details of M. Tech. Dissertation:

Title:

Institute/University:

Name of Supervisor:

12. Knowledge of MATLAB: Yes/No

13. Knowledge of simulation with other tools: Yes/No

14. Knowledge of Embedded programming: Yes/No

15. Knowledge of Python programming: Yes/No

16. Details of employment (if any):

Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty	Any other detail

17. Any other relevant information that you may like to furnish:

I declare that the above-given information is true to the best of my knowledge and belief. If any discrepancy is observed, my selection may be cancelled at any stage.

**Name and Signature of the Applicant**

For any queries regarding the position, please email [acidapumahesh@eed.svnit.ac.in](mailto:acidapumahesh@eed.svnit.ac.in)

A soft copy of the scanned application form with supporting documents may be sent via email to [acidapumahesh@eed.svnit.ac.in](mailto:acidapumahesh@eed.svnit.ac.in) on or before **10-09-2025**. Original documents, including age proof, certificates, degrees, mark sheets, and other testimonials, must be presented at the time of the interview.



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**Annexure II**

**Research Publications**

*(Name of the authors, Title of the paper, Journal/Conference name, volume, issue, Page Number)*