

Date: 15/04/2025

Advertisement for Research Personnel in the Google Sponsored Project

Applications are invited from eligible candidates for the position of Research Personnel (Senior Project Associate) in a sponsored research project funded by Google Asia Pacific Pte Ltd, Singapore, under the supervision of Dr. Abhishek Acharya. The duration of the position is a maximum of three years. The details of the project are mentioned below:

Title of the project	Development of IoT Edge Computing Applications using Optimized Near Threshold Voltage (NTV) Standard Cells with OPENLANE
Name & address of sponsoring agency	Google Asia Pacific Pte Ltd, Singapore
Research Personnel	Senior Project Associate
No. of post	01
Duration	Initially for a period of 01 year, may be extended up to 03 Years
Principal Investigator	Dr. Abhishek Acharya, Department of Electronics Engineering, Sardar Valllabhbhai National Institute of Technology Surat-395007 (Gujarat)
Essential Qualification	Senior Project Associate First Class M.E. /M.Tech. and B.E/B.Tech in Microelectronics & VLSI, VLSI & Embedded Systems, Electronics Engineering, Electronics & Communication Engineering, or related Allied branches with four years of experience in research and development in industrial and academic institution or science and technology organization and scientific activities and services.
Desirable Qualification	Background in Microelectronics & VLSI Design, knowledge of EDA Tools, Linux platform.
Fellowship	Rs. 42000/- per month
How to apply	A soft copy of the scanned application form with supporting documents may be sent via email <u>abhishek@eced.svnit.ac.in</u> on or before <u>09-05-2025</u> . Original documents, including age proof, certificates, degrees, mark sheets, and other testimonials, must be presented at the time of the interview. Date of National Level Test/Interview, will be communicated through email).
Address of Correspondence	Abhishek Acharya, Associate Professor Department of Electronics Engineering, Sardar Vallabhbhai National Institute of Technology Surat Ichchhanath, Dumas Road - 395007, Gujarat, India



Interested candidates are required to note the following:

- 1. The interested candidates will be required to submit their application form along with all essential details and scanned copy of all their original certificates, mark sheets and a recent passport size photograph by the last date, *i.e*, 09/05/2025, 05:00 PM. Clearly state the post applied for (Senior Project Associate) in the application form (Annexure 1).
- 2. Candidates need to bring the complete hardcopy set of the application form with xerox copies of all required documents on the day of interview for verification purpose.
- 3. Candidates are required to positively mention their e-mail ID and Mobile Number in the Application Form.
- 4. The applications received within the last date & time stipulated will be screened by a selection committee, and a list of eligible candidates shortlisted for National Level test / Interview will be communicated through email / institute website. The Screening Committee may devise its own criteria for shortlisting of candidates for interview.
- 5. The decision of SVNIT-Surat in all matters relating to eligibility, acceptance, screening, mode of selection will be final and binding on the candidates and no enquiry correspondence will be entertained in this connection from any individual or any agency on behalf of the candidate.
- 6. The original documents of selected candidates will be verified at the time of Interview, and in the event of any information mentioned in application/CV/testimonials found false or incorrect, their candidature straightway shall be cancelled.
- 7. No TA/DA will be paid for attending the interview/test.
- 8. For any queries regarding the position, please email Dr. Abhishek Acharya at <u>abhishek@eced.svnit.ac.in</u>.

Director