



**डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**  
**सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत**  
**SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT**

क्रमांक: DECE/C2S449/2024-25

Date: 11/11/2024

**Ministry of Electronics & Information Technology (MeitY) sponsored project:**  
**Secure and Energy Efficient Mixed Domain Compute in Memory-based AI Accelerator Chip for Edge Applications**

**Recruitment of Project Staff on Purely Contract Basis**  
**List of Shortlisted Candidates (Junior Research Fellow)**

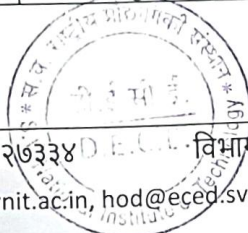
Sr. No.	Name of Candidate	Eligible/Not eligible	Remarks
1.	Sandip Kumar Singh	Eligible	-
2.	Rohit kumar Saket	Not Eligible	Not qualified in GATE/NET
3.	Sai Bharadwaj Kala	Eligible	-
4.	Joshi Priyanka M	Not Eligible	Not qualified in GATE/NET
5.	RINKAL SINGH	Eligible	-
6.	Sanket Kanani	Not Eligible	Not qualified in GATE/NET
7.	Prajapati Priyankkumar H.	Eligible	

**List of Shortlisted Candidates (Project Associate-I)**

Sr. No.	Name of Candidate	Eligible/Not eligible	Remarks
1.	Mombasawala Aleemuddin	Eligible	-
2.	Sukhjinder Singh	Not eligible	Qualification does not match
3.	Agarwal Rahul Surendra	Eligible	-
4.	Chauhan Jaydeep Hitendrabhai	Eligible	-
5.	Niket Gopal Singla	Eligible	-
6.	Saurabh	Eligible	-
7.	Vaibhav Acharya	Eligible	-
8.	Alisha Raut	Eligible	-

**List of Shortlisted Candidates ((Senior Project Associate)**

Sr. No.	Name of Candidate	Eligible/Not eligible	Remarks
1.	PRASHANT FAKIRA CHAUDHARI	Eligible	Age Proof required
2.	MAHESH MANOHAR BHAT	Eligible	Age Proof required
3.	Prajapati Priyankkumar H.	Eligible	-
4.	Malav Jaysukhbhai Sutariya	Not Eligible	Not sufficient R&D experience





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1. Interview for the shortlisted candidates will be held on November 12<sup>th</sup>, 2024 from 02:30 PM (through online mode). Although, *Candidates can come physically and report by 02:00 PM in Department Office.*
2. All candidates must produce relevant original certificates, experience letter (if any) and 2 photographs at the time of interview.
3. Candidate employed in institute/Industry must produce No-Objection Certificate (NOC) at the time of interview.
4. **No TA/DA will be paid for appearing in the interview** and/or joining the position.
5. List of selected candidates will be displayed on the institute website within one week after interview held.



  
**Dr. Pinalkumar Engineer**  
CI, C2S Project