**Annexure-I**

**Application form for the post of JRF**

**Application for the post of JRF** at Department of Electrical Engineering, SVNIT, Surat, Gujarat

**Title of project:** Implementation of Novel Speed Control Strategies for Photo Voltaic based Permanent Magnet Assisted Synchronous Reluctance Motor (PMaSyRM) Drives in Electric Vehicles

|  |  |  |
| --- | --- | --- |
| **For office use only:** Serial Number: Eligible for Interview: YES/NO Checked the Certificates: |  | Paste self-attested recent photograph |

**1. Name:**

**2. Address:**

**3. Date of birth:**

**4. Category** (General/SC/ST/OBC)**:**

**5. Contact details:
 (i) Email ID: (ii) Mobile No.:**

**6. Educational qualifications:**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Examination Passed** | **Board/University** | **Subject** | **Year** | **Percentage/****CGPA** | **Division/****Class** |
| M.E./M.Tech. |  |  |  |  |  |
| B.Tech. |  |  |  |  |  |
| 12th/Diploma |  |  |  |  |  |
| Other |  |  |  |  |  |

**7. B. Tech project details:**

**8. M. Tech project details:**

**9. Professional experience:**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Organization**  | **Post held** | **From** | **To** | **Pay Drawn** | **Nature of Duties** |
|  |  |  |  |  |  |

**10. Publications (If any) (add extra sheet if required):**

**11. Experience relevant to the field of project (as per title of the project):**

**12. Why do you feel that you are the best candidate for this position?**

**13. Do you have interest in joining Ph.D. program? (Yes/No):**

**14. GATE details:**

**15. Additional information if any, which you would like to mention in support of your suitability for the post, (attach separate sheet if necessary)**

**16. Recommendation** (Optional)**:** *(Provide complete details like name, address, email ID and contact number)*

**Declaration:**

I have provided correct information as above and I understand that, if found incorrect, I may be disqualified for the position.

**Date:**

**Place:**

**(Signature of the applicant)**

**Note:** Candidate need to submit their application form along with all essential details and scanned copy of required documents through the following link within the last date. The filled application along with all enclosures should reach the following address by 25/03/2024, 05.00 PM Candidate need to bring the complete hardcopy set of the application form with xerox copies of all required documents on the day of interview/test.

**Address:**

Dr. Kunisetti V. Praveen Kumar,

EN 321, Department of Electrical Engineering,

Sardar Vallabhbhai National Institute of Technology, Surat-395007, Gujarat, India