



डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग  
DEPARTMENT OF ELECTRONICS ENGINEERING  
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नॉलॉजी, सूरत  
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

क्रमांक: DECE/ARDB/776-A/ 2024-25

दिनांक: 30/07/2024

**Recruitment of Project Staff on Purely Contract Basis**

Applications are invited on prescribed format for the post of Senior Research Fellow/Senior Project Associate on purely contract basis for one year (may be extended till project duration) for Aeronautics R&D Board, DRDO, Ministry of Defence sponsored project titled "Development of Anti-jamming and Anti-Spoofing baseband signal processing algorithms for GNSS receiver provisioned UAVs" at the institute. Duly filled and signed application form along with self-attested scanned B.Tech./ M. Tech. / Ph. D. mark-sheets of all semesters, relevant experience certificates and necessary documents must be submitted in a **single PDF** file by email at [suresh.dahiya@eced.svnit.ac.in](mailto:suresh.dahiya@eced.svnit.ac.in) to on or before **30<sup>th</sup> August, 2024**. Applications received after said date will be not considered.

**Eligibility Criterion:**

Post	Qualification	Experience
Senior Research Fellow OR	Post Graduate Degree in Basic Science OR Graduate/Post Graduate Degree in Professional Course (in Electronics and allied areas) selected through a process described through anyone of the following: i) Scholars who are selected through National Eligibility Tests – CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. ii) The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISAR etc. AND Two years of research experience.	<b>Basic:</b> GNSS receiver, Signal processing, embedded computing, communication systems. <b>Software skills:</b> Xilinx Vivado tool-chain, MATLAB, embedded C.
Senior Project Associate	i) Bachelor's Degree in Engineering or Technology in (in Electronics and allied areas) from recognized university or equivalent AND ii) Four Years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organizations and Scientific activities and services OR Doctoral Degree in Science/ Engineering/ Technology/ MS from a recognized university or equivalent	

<b>No. of Posts / Duration:</b>	1-Post / Initial appointment for 1 year; extendible till project duration
<b>Consolidated Pay (Per Month):</b>	Rs. 42,000/- p.m. + HRA

**Note:**

- The above position is purely contractual for one year and can be extended till project duration based on the performance evaluation every year. The above-mentioned qualifications are minimum requirements. In case of large number of applications, additional short-listing criteria may be applied by the selection committee.
- The last date for receiving applications by email is **30<sup>th</sup> August, 2024**.
- The list of shortlisted candidates will be displayed on the institute website <http://www.svnit.ac.in> on **2<sup>nd</sup> September, 2024**.
- Interview for the shortlisted candidates will be held on **5<sup>th</sup> September, 2024** in online mode. However, final date and time for Interview will be communicated in the intimation email.
- Hard copies of documents are not required for interview. Original documents will be required before joining if selected.
- List of selected and waiting list candidates will be displayed on the institute website within one week after interview held. A waiting list candidate may be offered the position if the selected candidate fails to join the position because of any reason including inability to produce original certificates.
- The candidate completing the research work will be provided certificate and recommendation letter by Project Investigator to apply for relevant positions in allied Government Departments.
- Candidate who got selected may be allowed to enroll for Ph.D. program subject to the fulfillment of eligibility conditions of SVNIT, Surat.

**DIRECTOR**  
SVNIT, Surat



**APPLICATION FORM**

1.	Post applied for		Self Attested Photograph			
2.	Name of the Candidate					
3.	Address of the Candidate					
4.	Father's Name					
5.	Date of Birth					
6.	Age as on last date of application	____ Years ____ Months ____ Days				
7.	Contact No. (Mobile)					
8.	Email Id.					
9.	<b>Educational Qualification (Photocopy of certificate/degree must be attached)</b>					
	<b>Qualification</b>	<b>Subject/Discipline</b>	<b>Board/Institute /University</b>	<b>Year</b>	<b>% of marks/ CGPA obtained (Aggregate)</b>	
	10 <sup>th</sup> or equivalent					
	12 <sup>th</sup> or equivalent					
	Bachelor Degree					
	Master Degree					
	Ph.D.					
	Master Degree Thesis Title					
	Ph.D. Thesis Title					
8.	<b>Relevant Experience (Details of Employment in chronological order. Enclose a separate sheet duly authenticated under your signature if necessary)</b>					
	<b>Organization</b>	<b>Post Held</b>	<b>From</b>	<b>To</b>	<b>Pay Drawn</b>	<b>Nature of Duties</b>
9.	<b>Total emoluments per month presently drawn.</b>					
10.	<b>Additional information if any, which you would like to mention in support of your suitability for the post, (attached separate sheet if necessary)</b>					

**Declaration**

I hereby declare that the information furnished above is true to the best of my knowledge and belief. If at any time it is found that I have concealed any information or have given any incorrect data, my candidature/appointment, may be cancelled/terminated, without any notice or compensation.

Place:  
Date:

Signature of the Candidate