## DEPARTMENT OF ELECTRONICS ENGINEERING

## Walk-in-Interview for the posts of Teaching Assistants (purely on ad-hoc basis for 11 months)

The institute is going to conduct a walk-in-interview in offline mode (written test (If required) and interview) for the post of Teaching Assistant (purely on ad-hoc basis for 11 months) in the Department of Electronics Engineering on **15**<sup>th</sup> **February 2023** (Reference: No. DECE/TT2022\_EVEN\_TA/1928/2022-23 dated: 27/01/2023).

The minimum qualifications and remuneration to be paid to the Teaching Assistant(s) purely on ad-hoc basis is as given below:

Sr. No.	Category	Monthly remuneration
1	1st Class or an equivalent CGPA (Min 6.5 on the scale of 10) in M. Tech /	Rs.45,000/-
	M.E. and 1st Class or an equivalent CGPA (Min 6.5 on the scale of 10) in	
	B. Tech / B.E with Good Academic Record in the Electronics Engineering.	
2	Ph. D. (in appropriate branch) with 1st Class or an equivalent CGPA (Min	Rs.60,000/-
	6.5 on the scale of 10) in B. Tech / B.E and 1st Class or an equivalent CGPA	
	(Min 6.5 on the scale of 10) in M. Tech / M.E.(Engg.) with Good	
	Academic Record in the Electronics Engineering.	

The prescribed application form is available on the institute website.

The schedule for walk-in-interview is as follows:

Walk-In-Interview Process	Date: 15 <sup>th</sup> February 2023	Venue: Department of Electronics Engineering
Reporting	9:30 am	Conference Room, DECE (Ground Floor)
Document Verification, Written Test (If required) & Interview	9:45 am	

- Though this is walk-in-Interview the candidates are advised to send an advance soft copy of the complete application in a single PDF to office@eced.svnit.ac.in on or before 14/02/2023, 12:00 noon.
- The candidate is requested to remain present with a filled application in the prescribed form only along with original supporting documents such as degree certificates, semesterwise marks sheets, experience certificates, testimonials, etc. and one set of self-attested copy of the same documents at 9:00 am in the department of electronics engineering on 15<sup>th</sup> February 2022(Wednesday). For the same level of performance, preference will be given to the reserved category and PwD category candidates.
- Any wrong or misleading information supplied by the candidate may lead to summarily rejection of the application/appointment, if found subsequently.
- Applications on plain paper or incomplete applications will not be accepted.