



DEPARTMENT OF ELECTRONICS ENGINEERING
डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नॉलॉजी, सुरत

क्रमांक: DoECE / Ph.D. Adm/ 104 / 2025-26

दिनांक: 22/04/2026

Ph. D. Admission (Academic Year-2025-26)

List of Eligible/ Provisionally Eligible Candidates for the DoECE, May – 2026

Sr. No.	App. ID	User Name	Candidate's Name	Ph.D. Cat.	Eligible / PE / NE
1.	13170	SVNIT3PQRY	Ansari Sharique Jamalshah Nawaz	ERS (PART TIME)	NE
2.	13172	SVNIT3PVXB	Yudhistira Behera	ERS (PART TIME)	PE
3.	13187	SVNIT3PB96	Gorde Snehal Hausiram	ERS (SELF FINANCE)	PE
4.	13232	SVNIT3PLEI	Bhupendranath Chaudhary	FIR	PE
5.	13241	SVNIT3PVKG	Jagtap Devidas Arjun	ERS (PART TIME)	PE
6.	13279	SVNIT3PH2F	Sonawane Sarika Shekhar	ERS (PART TIME)	PE
7.	13415	SVNIT3PCNW	Baranikunta Harikrishna	ERS (PART TIME)	PE
8.	13460	SVNIT3PCMO	Kanade Kiran Abasaheb	ERS (PART TIME)	PE
9.	13511	SVNIT3P4KL	Shubhangi Sudhakar Bachhav	ERS (PART TIME)	PE
10.	13526	SVNIT3PVD9	Annapureddy Naresh	ERS (PART TIME)	PE
11.	13552	SVNIT3PP9K	Dhrubojyoti Dey	FIR	PE
12.	13661	SVNIT3PJOT	Ijaz Ahmed	FIR	E
13.	13665	SVNIT3PBDW	Praful Bhagat	ERS (PART TIME)	PE

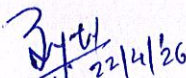
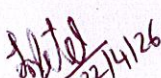
Note: E - Eligible, PE- Provisionally Eligible, NE- Not Eligible


- The candidates must produce original documents with relevant **mark-sheets of all semesters** as well as **final degree certificate** of B.E./B. Tech. and M.E./M. Tech., B.Sc/M.Sc. at the time/day of the interview. Failing which they will not be allowed to participate in admission process.
- Candidates applied under OBC category must produce **recent(April-2025 onwards) NCL** in English language.
- Candidates who have applied in FIR category need to produce the **GATE/NET Score card**.
- All **PE** candidates applied under **ERS(Part Time)** category for Ph.D.Admission must enclose self-attested copies of the following documents/certificates. Same must be produced in original at the time of interview/physical documents verification.
 - Appointment letter and sponsorship certificate/no objection certificate issued by the current employer
 - Experience certificate duly signed by competent authority on the letter head of the Company/Organization/Institute.
 - The external candidates should produce fresh NOC/Sponsorship letter strictly as per format in Appendix III and VII.
 - Other documents like date of birth and identity proofs, marksheets of all semesters, degree certificates.

➤ For any queries contact office@eced.svnit.ac.in

Instruction For Candidates:

- The above Candidates are requested to Report at the Department of Electronics Engineering on **06/05/2026 at 9:00 AM** with **all Original Documents**. **Short-listed candidates must remain present for the interview/presentation at Room No. EC004N Conference room, Ground Floor.**
- The Presentation/ interview will **start at 11:00 AM**. Candidates are required to give a presentation as per the following guidelines.
 - The presentation should not be more than a maximum of 10 minutes duration.
 - A PowerPoint presentation is a must.
 - The maximum number of slides including the title slide should not be more than 8.
 - The presentation should contain the background of topic, literature survey, research gap, problem identification and research methodology.
- Provisionally eligible candidates will be allowed to appear for the interview/presentation only if they submit appropriate documents at the time of document verification (with a self-attested copy).


22/4/26

22/4/26
Dr. Z. M. Patel, Dr. J. N. Patel
Ph. D In-charges


22/4/26
Dr. (Ms.) Shilpi Gupta
Head, DoECE