



सरदार वल्लभभाई राष्ट्रीय प्रौद्योगिकी संस्थान, सूरत  
SARDAR VALLBHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT  
विद्युत इंजीनियरिंग विभाग  
DEPARTMENT OF ELECTRICAL ENGINEERING

SVNIT

No: DoEE/Ph.D/ 95 /2024-25

DATE: 17/04/2024

List of Eligible/ Provisionally Eligible candidate for Ph.D. Admission (Phase-I),  
Academic Year-2024-25,

S.No	Application ID	Name	Ph.D. Category	Status of Eligibility	Remarks
1	SVNIT3P7HQ	Arun Alawa	FIR	Provisionally Eligible	<ul style="list-style-type: none"><li>The certificate and marksheet of SSC, B.Tech, and M.Tech degree(s) are required.</li></ul>
2	SVNIT3POMA	Aditya D. Kadiya	FIR	Provisionally Eligible	<ul style="list-style-type: none"><li>The certificate and marksheet of M.Tech degree are required.</li><li>OBC-NCL certificate as per central govt. format is required.</li><li>Undertaking letter not attached.</li></ul>
3	SVNIT3PN8A	Nancy Kumari	FIR	Provisionally Eligible	<ul style="list-style-type: none"><li>Candidate Name needs to be corrected in application.</li><li>OBC-NCL certificate as per central govt. format is required.</li></ul>
4	SVNIT3PNEE	Jaiminkumar P. Patel	FIR	Provisionally Eligible	<ul style="list-style-type: none"><li>OBC-NCL certificate as per central govt. format is required.</li></ul>
5	SVNIT3PGBQ	Brahmbhatt Kenil Dilipbhai	FIR	Provisionally Eligible	<ul style="list-style-type: none"><li>OBC-NCL certificate as per central govt. format is required.</li></ul>
6	SVNIT3PKEB	Parmar Raviraj Girishbhai	FIR	Eligible	NIL
7	SVNIT3PUEZ	Raktangi Swain	FIR	Provisionally Eligible	<ul style="list-style-type: none"><li>Gate score card not attached.</li></ul>
8	SVNIT3P9NH	Arvind Kumar Pandey	FIR	Provisionally Eligible	<ul style="list-style-type: none"><li>The certificate and marksheet of M.Tech degree are required.</li></ul>
9	SVNIT3PWVG	Bhupendra Deshmukh	EXTERNAL	Provisionally Eligible	<ul style="list-style-type: none"><li>Latest Experience certificate,</li><li>One Year salary slips/ ITR,</li><li>NOC, Sponsorship letter required.</li></ul>

### Instructions to the Candidates:

1. The candidates have to report for document verification at the Department of Electrical Engineering Seminar Hall ( Ground Floor) at 9:00 AM on May 09, 2024.
2. All the candidates are required to produce the list of document mentioned below in ORIGINAL during the document verification, failing which the candidate will not be allowed for presentation/interview.
3. At the time of document verification, the candidates are also required to submit self-attested copies of the document mentioned in the Remarks Column against their name.
4. The candidates will have to give presentation (of about 10 minutes) on the topic in which he/she aspired to procure his/her research work, which will be followed by an interview.

### List of Documents:

The FIR candidates are required to bring the following ORIGINAL documents along with their self-attested photocopies

1. Document for proof of date of birth
2. Photo ID proof as per Government of India norms.
3. Degree certificate of B.E./ B. Tech.
4. Grade/Mark sheets of qualifying examination of all semesters (M.E./ M.Tech).
5. Degree/ Provisional certificate (M.E./M.Tech).
6. GATE score card
7. Certificate of category (SC/ST/OBC-NCL/EWS) issued by the competent authority.  
**In case of OBC-NCL/EWS category, the certificate must be issued on or after April 01, 2024.**
8. Two passport size recent photographs.

The ERS candidates are required to bring the following ORIGINAL documents with their self-attested photocopies.

1. Sr. No 1 to Sr. No. 5 as mentioned above (in case of FIR candidates)
2. Two passport size recent photographs.
3. Appointment letter issued by the present employer
4. Experience certificate
5. Salary certificate / slips of last 12 months
6. Income tax return or Form-16 of last financial year
7. Sponsorship certificate (ignore, if original document is already submitted)
8. Any other documents as mentioned in the information brochure of PhD admission.

*Pardeep Singh*

17-04-2024

Head of Department

विभागाध्यक्ष / Head

इलेक्ट्रीकल इंजीनियरींग विभाग

Department of Electrical Engineering

म.व.रा.पौ.सं., सुरत-७ / S.V.N.I.T., Surat-7