



DEPARTMENT OF ELECTRONICS ENGINEERING
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ़ टेक्नोलॉजी, सुरत

क्रमांक: ECED/Mid-Sem/Spring/2024/2021-22

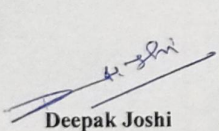
दिनांक: 01/03/2022

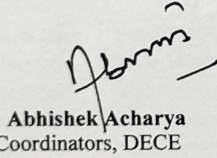
M. Tech. 2nd Semester / Ph. D Mid - Semester Exam (March – 2022)

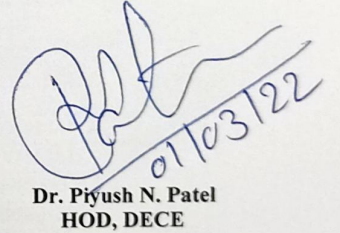
Date	Subject (11:30 am to 01:30 pm)	Course Coordinators
07/03/2022	ADHOC Network (EC636)	U. D. Dalal (98980 57727)
08/03/2022	Analog VLSI Design (EC612)	R. N. Dhavse (9723815535)
09/03/2022	Real Time Systems (EC614)	P. J. Engineer (9924711335)
	MIMO Technology (EC638)	S. Gupta (9737011973)
10/03/2022	Testing & Verification of VLSI Circuits (EC644)	D. Joshi (7896171659)
	Image Processing (EC602)	K. P. Upla (9998969067)
11/03/2022	VLSI System Design (EC642)	A. D. Darji (9426886446)
	Optical Network (EC608)	A. S. Mandloi (70167 86449)
12/03/2022	Wireless Communication (EC604)	S. Dahiya (9571044802)

Note:

- The Mode of Examination and Online Platform are to be informed by the Subject Coordinators.
- The examinations will be conducted as per the Circular No. Acad/Mid-Sem/Spring/1510/2021-22, dated 28/02/2022.


Deepak Joshi


Abhishek Acharya
Examination Coordinators, DECE


Dr. Piyush N. Patel
HOD, DECE

Copy to:

- 1) Dy. Registrar (Acad)
- 2) Chief Exam Coordinator
- 3) Student NB ECED
- 4) upload@svnit.ac.in
- 5) All concerned Departments

फोन नं: संस्थान कार्यालय: २२२३७१-७४, फैक्स नं: २२२८३९४, २२२७३३४

विभागीय प्रमुख: २२०१५५१, विभाग कार्यालय: २२०१५५२

ई-मैल: director@svnit.ac.in, hod@eced.svnit.ac.in