



DEPARTMENT OF ELECTRONICS ENGINEERING
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

क्रमांक: DoECE/Make Up/ **1604** /2025-26

दिनांक: 12-11-2025

Make Up Mid-Semester Exam Schedule (November-2025)

B. Tech. II – Electronics & Communication Engineering
(3rd Semester)

Exam Date	Time (11:15 AM to 12:45 PM)
17-11-2025	Principles of Communication Systems(EC207)
18-11-2025	Professional Ethics, Economics, and Business Management(MG210)
19-11-2025	Microprocessor & Microcontroller(EC205)
20-11-2025	Analog Circuits(EC201)
21-11-2025	Signals and Systems(EC203)

B. Tech. II – Electronics and VLSI Engineering
(3rd Semester)

Exam Date	Time (11:15 AM to 12:45 PM)
17-11-2025	Microprocessor & Microcontroller(VL205)
18-11-2025	Professional Ethics, Economics, and Business Management(MG210)
19-11-2025	Control Systems(EE259)
20-11-2025	Analog Circuits(VL203)
21-11-2025	Digital Signal Processing(VL201)

B. Tech. III – Electronics & Communication Engineering
(5th Semester)

Exam Date	Time (11:15 AM to 12:45 PM)
17-11-2025	Digital Communication(EC301)
18-11-2025	Digital Signal Processing(EC303)
19-11-2025	Data Communication Networks(EC321)
	Computer Architecture and Organization(VL321)
	Embedded Systems(VL323)
20-11-2025	Digital Image Processing(EC341)
	Antenna Theory(EC343)
	Hardware Description Language(VL343)
21-11-2025	Sensors and Transducers(EC361)
22-11-2025	Digital Electronics and Microcontrollers(EC311)
	Probability and Random Processes(EC313)

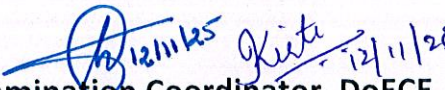
B. Tech. IV – Electronics & Communication Engineering
(7th Semester)

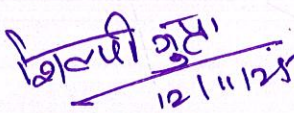
Exam Date	Time (09:00 AM to 10:30 AM)
17-11-2025	Microwave Engineering(EC401)
18-11-2025	Information Theory and Coding(EC403)
19-11-2025	Optical Wireless Communication(EC431)
	Processor Architecture(EC435)
	EM Interference and Compatibility(EC437)
20-11-2025	Deep Learning(EC421)
	MIMO Communication systems(EC423)
	Intelligent Systems and Robotics(EC427)
21-11-2025	Fundamentals of Nano electronics(EC451)
	Testing and Verification of VLSI Circuits(EC453)
	Ad-Hoc Networks(EC455)

Seating Arrangement: Room No: - EC117N, BEC Lab, 1st Floor, DoECE

Note: -

- Students has to **report 15 minutes** before the commencement of the respective examination.
- Exchange of writing materials, stencils, mathematic instruments etc. are strictly prohibited. Carrying Pager, Digital Diary, Cellular Mobile phone, Programmable calculator, Smart watch and other electronic Gadget in the examination hall is also prohibited. Any student found with prohibited materials, disciplinary action will be initiated against the student.
- Do not bring any book, notes or scribbling paper into the examination hall. Do not speak or communicate in any other way and do not take any answer book whiling leaving the examination hall otherwise disciplinary action will be initiated.
- Students will **not be allowed to go outside the Classroom** for the **whole duration of the exam**.


Examination Coordinator, DoECE


HOD, DoECE

Copy to:

- 1) Dy. Registrar (Acad) 2) Student NB DoECE 3) upload@svnit.ac.in 4) All concerned Departments
- In case any query, contact Examination Coordinators, DoECE**