



End-Semester Exam Schedule (December- 2022)

M. Tech.-I/ Ph. D.- I (CS / VLSI)

Exam Date	Time (09:30 AM to 12:30 PM)
19/12/2022	Digital VLSI Design (EC609)
20/12/2022	VLSI Technology (EC617)
21/12/2022	Advance Optical Communication Systems (EC603)
	MOS Devices & Technology (EC611)
22/12/2022	Information Theory & Coding (EC619)
	Embedded System (EC613)
23/12/2022	Advance Digital Signal Processing (EC605)
24/12/2022	RF Circuit & System (EC601)

Note:

- The examinations will be conducted as per the Circular No. Acad/End-Sem/Autumn/170/2022-23, dated 30/11/2022.
- All the students have to compulsorily carry their Hall Tickets during the examination.

Dr. Deepak Joshi
Examination Coordinators, DoECE

Dr. Piyush N. Patel
HOD, DoECE

Copy to:

- 1) Dy. Registrar (Acad)
- 2) Chief Exam Coordinator
- 3) Student NB ECED
- 4) upload@svnit.ac.in
- 5) All concerned Departments

प्रभारी विभागाध्यक्ष /In-charge Head
इलेक्ट्रॉनिक्स इंजीनियरिंग विभाग
Department of Electronics Engineering

In case any query, contact Examination Coordinators, DECE.