

TIME TABLE

Semester: ODD Year: 2026-2027

Class: B. TECH-III VL-A

Day	1	2	3	4		5	6	7	8	9
	8.30 - 9.20	9.30 - 10.20	10.30 - 11.20	11.30 - 12.20	12.20 - 2.00	2.00 - 2.50	3.00 - 3.50	4.00 - 4.50	5.00 - 5.50	6.00 - 6.50
Monday			C3 (DE-I)	D4 (S&T)		H5 (SIT)	I6 (VD)			
Tuesday			D3 (VD)	E4 (S&T)						
	P3 (DE-I)					P13 (SIT-A1)				
Wednesday	C1 (DE-I)	D2 (SIT)	E3 (DE-II)	A4 (S&T)						
						P15 (VD-A1)				
Thursday			A3 (DE-II)					G7 (PRP)	H8 (PRP)	
						P17 (VD-A2)		P18 (DE-I)		
Friday	E1 (DE-I)	A2 (SIT)	B3 (VD)	C4 (DE-II)				H7 (PRP)	I8 (T) (PRP)	
						P19 (SIT-A2)				

Sr. No.	SLOT	COURSE NO	TITLE	FACULTY	CREDIT	CLASS ROOM
(1)	H5,D2,A2	VL301	Semiconductor IC Technology	VG,VG,VG	3-0-2	EC105N
(2)	I6,D3,B3	VL303	VLSI Design	ADD,ADD,ADD	3-0-2	EC105N
(3)	DE-I C3,C1,E1	EC321	Data Communication Networks	RP,RP,RP	3-0-2	EC106N
(4)		VL321	Computer Architecture and Organization	SM,SM,SM	3-0-2	EC201N
(5)		VL323	Embedded Systems	TA2,TA2,TA2	3-0-2	EC105N
(6)	DE-II E3,A3,C4	VL341	Semiconductor Device Modeling	NC,NC,NC	3-0-0	EC202N
(7)		VL343	Hardware Description Language	SM,SM,SM	3-0-0	EC203N
(8)	D4,E4,A4	EC361	Sensors and Transducers	PNP,PNP,PNP	3-0-0	EC106N
(9)	P13	VL301	Semiconductor IC Technology-A1	VG,L1,P7	3-0-2	EC303N
(10)	P19	VL301	Semiconductor IC Technology-A2	VG,L1,P7	3-0-2	EC303N
(11)	P15	VL303	VLSI Design-A1	ADD,VG,L1,P4	3-0-2	EC306N
(12)	P17	VL303	VLSI Design-A2	VG,L1,P4	3-0-2	EC306N
(13)	P3,P18	EC321	Data Communication Networks	RP,P5 & RP,P5	3-0-2	EC406N
(14)	P3,P18	VL321	Computer Architecture and Organization	SM,P8 & SM,P8	3-0-2	EC306N
(15)	P3,P18	VL323	Embedded Systems	L2,L3 & PJE,L2	3-0-2	EC307N
(16)	G7,H8,H7,I8	EC313	Probability and Random Processes (Honors)	KC,KC,KC (T):KC,P13	3-1-0	EC105N

