



DEPARTMENT OF ELECTRONICS ENGINEERING
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ़ टेक्नोलॉजी, सूरत

क्रमांक: DoECE/suppli./ **637** /2024-25

दिनांक: 30-06-2025

Supplementary / End Semester Exam Schedule (July-2025)
Students Under NEP Scheme

**M. Tech. I – Electronics & Communication Engineering
(1st Semester)**

Exam Date	Time (02:30 PM to 05:30 PM)
07-07-2025	Digital VLSI Design(ECVL101)
08-07-2025	Advance Optical Communication Systems(ECCS103)
	Testing and Verification of VLSI Circuits(ECVL117)
09-07-2025	Advance DSP(ECCS105)
10-07-2025	Information Theory & Coding(ECCS111)
	Embedded Systems(ECVL105)
11-07-2025	Digital Satellite Communication(ECCS113)
	Semiconductor IC Technology(ECVL111)
12-07-2025	RF Circuits & Systems(ECCS101)
	MOS Devices and Technology(ECVL103)

**M. Tech. I – Electronics & Communication Engineering
(2nd Semester)**

Exam Date	Time (02:30 PM to 05:30 PM)
14-07-2025	Image Processing and Computer Vision(ECCS102)
	VLSI System Design (ECVL116)
15-07-2025	5G Wireless Technologies (ECCS128)
	Analog VLSI Design (ECVL102)
16-07-2025	Speech Processing and Applications (ECCS130)
	SOC Design (ECVL118)
17-07-2025	Wireless Communication(ECCS104)
	Real-Time Systems (ECVL104)
18-07-2025	Deep Learning, Theory and Practice (ECCS172)
	Fundamentals of Semiconductor Package Manufacturing and Test (ECVL178)

Seating Arrangement: EC022N (Ground Floor, DoECE)

Note:

- The examinations will be conducted as per the Circular No. Acad/ Suppl. Exam/146/2024-25, dated: 25/06/2025.
- Students has to report 15 minutes before the commencement of the respective examination.
- Exchange of writing materials, stencils, mathematic instruments etc. are strictly prohibited. Carrying Pager, Digital Diary, Cellular Mobile phone, Programmable calculator, Smart watch and other electronic Gadget in the examination hall is also prohibited. Any student found with prohibited materials, disciplinary action will be initiated against the student.
- Do not bring any book, notes or scribbling paper into the examination hall. Do not speak or communicate in any other way and do not take any answer book whiling leaving the examination hall otherwise disciplinary action will be initiated.
- Students will not be permitted for the Examination without valid Hall Ticket.

Examination Coordinator, DoECE

HOD, DoECE

Copy to:

1) Dy. Registrar (Acad) 2) Chief Exam Coordinator 3) Student NB DoECE 4) upload@svnit.ac.in 5) All concerned Departments
 In case any query, contact Examination Coordinators, DoECE



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क्रमांक: DoECE/suppli./ 696 /2024-25

दिनांक: 30-06-2025

Supplementary / End Semester Exam Schedule (July-2025)
Students Under Non-NEP Scheme

**M. Tech. I – Electronics & Communication Engineering
(1st Semester)**

Exam Date	Time (02:30 PM to 05:30 PM)
07-07-2025	Digital VLSI Design (EC609)
08-07-2025	Advance Optical Communication Systems (EC603)
	Hardware Description Language (EC623)
09-07-2025	Advance Digital Signal Processing (EC605)
10-07-2025	Information Theory & Coding (EC619)
	Embedded System (EC613)
11-07-2025	Digital Satellite Communication (EC621)
	VLSI Technology (EC617)
12-07-2025	RF Circuit & System (EC601)
	MOS Devices & Technology (EC611)

**M. Tech. I – Electronics & Communication Engineering
(2nd Semester)**

Exam Date	Time (02:30 PM to 05:30 PM)
14-07-2025	Image Processing (EC602)
	VLSI System Design (EC642)
15-07-2025	MIMO Technology (EC638)
	Analog VLSI Design (EC612)
16-07-2025	Optical Networks (EC608)
	Testing and Verification of VLSI Circuits (EC644)
17-07-2025	Wireless Communication (EC604)
	Real Time Systems (EC614)
18-07-2025	Ad-Hoc Networks (EC636)
	Low Power VLSI Design (EC622)

Seating Arrangement: EC022N (Ground Floor, DoECE)

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[Signature]
 11/7/25
 Examination Coordinator, DoECE

[Signature]
 11/7/25
 HOD, DoECE

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