

24-07-2025

TIME TABLE

Semester: ODD Year: 2025-2026

Class: M. TECH-I CS & VL

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30 - 9.20	9.30 -10.20	10.30 - 11.20	11.30 - 12.20	12.20 - 2.00	2.00 -2.50	3.00 -3.50	4.00 -4.50	5.00 - 5.50
Monday	A1(RFCS) (MOS)	B2 (ES)	C3(DSC)	D4(AOCS)		G5(ADSP) (T&V)	H6 (DVD)		
			P2(ES)						
Tuesday			D3 (DVD)	E4 (ITC)		H5(RFCS) (MOS)	I6 (T&V)		
Wednesday	C1 (ES)	D2(ADSP) (T&V)	E3(AOCS) (SIT)	A4(DSC)					
						P15 (RFCS & DVD)			
Thursday	D1(DSC) (MOS)	E2(AOCS) (SIT)	A3 (ITC)						
						P17 (AOCS)		P18 (MOS)	
Friday	E1(ADSP) (SIT)	A2 (ITC)	B3 (DVD)	C4 (ES)		K5(RFCS)			

Sr. No.	SLOT	COURSE NO	TITLE	FACULTY	CREDIT	CLASS ROOM
(1)	A1,H5,K5	ECCS101	RF Circuits & Systems	KI,KI,KI	3-0-0	EC303N
(2)	D4,E3,E2	ECCS103	Advance Optical Communication Systems	PNP,PNP,PNP	3-0-0	EC303N
(3)	G5,D2,E1	ECCS105	Advance DSP	PKS,PKS,PKS	3-0-0	EC303N
(4)	E4,A3,A2	ECCS111	Information Theory & Coding	JNS,JNS,JNS	3-0-0	EC222N
(5)	C3,A4,D1	ECCS113	Digital Satellite Communication	SNS,SNS,SNS	3-0-0	EC303N
(6)	H6,D3,B3	ECVL101	Digital VLSI Design	ADD,ADD,ADD	3-0-0	EC222N
(7)	A1,H5,D1	ECVL103	MOS Devices and Technology	L1,L1,L1	3-0-0	EC222N
(8)	B2,C1,C4	ECVL105	Embedded Systems	PJE,PJE,PJE	3-0-0	EC222N
(9)	G5,I6,D2	ECVL117	Testing and Verification of VLSI Circuits	DJ,DJ,DJ	3-0-0	EC222N
(10)	E3,E2,E1	ECVL111	Semiconductor IC Technology	VG,VG,VG	3-0-0	EC222N
(11)	P15	ECCS101	RF Circuits & Systems	KI,P9	0-0-6	EC403N
(12)	P17	ECCS103	Advance Optical Communication Systems	PNP,P22	0-0-6	EC403N
(13)	P3	ECCS105	Advance DSP	PKS,P5	0-0-6	EC307N
(14)	P15	ECVL101	Digital VLSI Design	ADD,P6	0-0-6	EC306N
(15)	P18	ECVL103	MOS Devices and Technology	VG,P3	0-0-6	EC306N
(16)	P2	ECVL105	Embedded Systems	PJE,L2	0-0-6	EC307N

