About the Institute

The institute was initially established as Sardar Vallabhbhai Regional College of Engineering & Technology in 1961 and was upgraded as a National Institute of Technology on 4th October, 2002. SVNIT is one of the pioneering engineering institutions of the country which has contributed many outstanding engineers in India & abroad. It is conducting six UG programs, seventeen PG programs (in addition to three integrated M.Sc. programs) and a Ph. D. program in all disciplines of engineering and applied sciences. Special attention is given to interdisciplinary research. The institute has an excellent placement record with a number of top ranking companies visiting the campus every year.

About the Department

The department is one of the pioneering departments of the Institute. Over the years, the department has progressed at a rapid pace with development in both the spheres of infrastructure facilities and academic programs. The department has highly qualified faculty members engaged in teaching and research with the aim of achieving excellence in the field of Electrical Engineering. The department offers an Undergraduate course in Electrical Engineering and Postgraduate programs in Power Electronics and Electrical Drives, Power Systems and Instrumentations and Control. The department offers a Ph. D. program to promote basic research activities in the various areas of Electrical Engineering. The consultancy and testing services are also rendered by the department.

About the Surat

Surat is a top ranking industrial city of the country with clean wide roads. It is well known worldwide for textiles, Zari and Diamond industries. Several large scale industries and establishments are located in the city. Surat is situated on the main western railway route between Vadodara and Mumbai. The institute is located at Ichchanath on Surat-Dumas road at a distance of about 10 km from Surat railway station.

About Training Program

The objective of the training is to develop the ability in the participant to generate the code for the hardware device TMS320F28335 DSP readily from the Code Composer Studio (CCS) and simulation developed in PSIM. The training includes the introduction to DSP TMS320F28335, CCS, Embedded Coder, and PSIM software. The migration of code from PSIM simulation to CCS and loading of the program in the hardware device is demonstrated. The code generation demos for the various peripheral devices like digital inputs, digital outputs, ADC, SPI based DAC, 1-phase PWM, 2-phase PWM and 3-phase PWM are included.

Major Highlights:

The eminent persons who were expertise in programming of TMS320F28335 with CCS, Embedded Coder, and PSIM software will introduce the basic programing concepts. In addition to the expert lectures from the resource persons, the initialization of TMS320F28335 DSP peripherals with application examples will be demonstrated with help of hands on based demonstrations.

Topics to be Covered:

- **Programming of TMS320F28335 DSP using Code Composer Studio:-** Basic DSP concepts, architecture and features of TMS320F28335, initialization and interfacing of peripherals like general purpose input output (GPIO) pins, Analog to Digital Converter (ADC), Digital to Analog Converter (DAC), eQEP and PWM generation for single switch and complementary switches with dead band.

- **Direct Code Generation using PSIM Software:** Introduction to PSIM software, Simcoder module of PSIM to generate code and Library elements of Simcoder module for TI 28335 DSP and code generation for GPIO, ADC, DAC and PWM generation.

Call for Participation

One-Week Short Term Training Program (Online)

Digital Signal Processor: An Introduction with Code Composer Studio and PSIM Software

14-16, 23-24 April 2022

Course Coordinators

Dr. Mahmadasraf A. Mulla
Dr. Suresh Lakhimsetty

Organized by
Department of Electrical Engineering
Sardar Vallabhbhai National Institute of Technology, Surat-395007, Gujarat, India

(An Institute of National Importance of Govt. of India)

Registration and General Information

The program will be organized through google meet. by the Advanced Power Electronics Lab (APEL) of the electrical engineering department. Applications for the participation in the 'course' should fill in the Google Form by using the following link.

REGISTRATION FORM

Instructions to fill the Google form

- In Google form all fields are mandatory.
- Participants have to attach the scanned copies of the Filled Registration Form and Payment Details.
- Alternatively, the participants can send the application on the following email id as well

apel@eed.svnit.ac.in

The last date of registration is

08 April 2022

The candidates would be informed of their selection through E-mail by

11 April 2022

The participants should attend all the sessions.

Address for any Communications

Dr. Mahmadasraf A. Mulla
Dr. Suresh Lakhimsetty

Organizing Committee
Department of Electrical Engineering
S. V. National Institute of Technology,
Ichchhanath, SURAT, Gujarat, 395007.

E-mail:
apel@eed.svnit..ac.in

Mobile: 9825113488, 7702759430

Course Fee

Research Scholars: UG/PG/Ph.D.   : 1200/-
Academicians/Scientists/Researchers   : 2000/-
Delegates from Industries   : 3000/-

The non-refundable registration fee should be sent through **Net-banking/Online Payment.**
Bank Account Name   :   Director, SVNIT-CCE
SBI Account No.   :   37030749143
Bank Name   :   State Bank of India
IFSC Code   :   SBIN0003320
Branch   :   SVRCET Branch, Ichchhanath, Surat, Gujarat, 395007.

While paying through the net-banking, in remarks the purpose is to be written as **"DSP-STTP Registration Form"**.

(Kindly save the receipt or take screenshot of the payment)

Who Can Apply?
- Research Scholars
- Teachers of Engineering Colleges
- Practicing Engineers from industries
- PG/UG students

Patron
- Prof. Ravipudi Venkata Rao, Director, SVNIT, Surat.

Organizing Committee
- All faculty members of EED, SVNIT, Surat.

Resource Persons
- Academicians from IITs/NITs and other Professionals

Other Instructions
- This STTP will be conducted through Google Meet platform, so the participants should be equipped with the necessary infrastructure.
- In case of any query, feel free to contact the course coordinators.
- Google meet link will be shared to participants prior to the session starts.
- The certificates will be issued based on their attendance in the technical sessions

Registration Form

One Week Short Term Training Program (STTP)
On
Digital Signal Processor: An Introduction with Code Composer Studio and PSIM Software

14-16, 23-24 April 2022

Full Name:

Designation:

Department and Institution with Address:

Male/Female:
Mobile:
Open/OBC/SC/ST:
E-mail (Gmail):
Date of Birth:
Academic Qualifications:

Experience (Years):
        Teaching:
        Research:
        Industry:

I declare that the details furnished above are correct to the best of my knowledge and belief. I also undertake to abide by the rules and other conditions prescribed by SVNIT, Surat.

Signature of the Applicant