

## PROGRAMME OBJECTIVE

The Department of Computer Engineering, S. V. National Institute of Technology is organizing TEQIP-III Sponsored one week Short Term Training Programme (STTP) on “ Deep Learning Applications (DLA-2019)” during 18<sup>th</sup> June – 22<sup>nd</sup> June, 2019. The workshop aims to give exposure to recent advances in the area of deep learning to the participants.

- ✚ Targets motivated scholars at Undergraduate (any year) / master / PhD Level and faculty members
- ✚ No pre-requisites required
- ✚ Practical Oriented Workshop
- ✚ Extensive Hands-on sessions

The course is aimed to cover following aspects with respect to Deep Learning.

- Basics of Machine Learning
- Machine Learning – Support Vector Machine
- Convolution Neural Network
- Auto Encoder and VAE
- Generative Adversarial Network
- Recurrent Neural Network
- LSTM

## ELIGIBILITY

The programme is open to the faculty members of engineering colleges, Research Scholar, and other allied disciplines. Industry personal working in the same discipline can also attend the program.

## SPEAKERS

Resource persons are from reputed NIT's and IIT's.

## PROFORMA FOR REGISTRATION FORM

Name: (In Block Letters) \_\_\_\_\_

Gender: \_\_\_\_\_

Designation: \_\_\_\_\_

Institutional Address: \_\_\_\_\_

Highest Qualification: \_\_\_\_\_

Area of Specialization: \_\_\_\_\_

Research Interests: \_\_\_\_\_

Address for Correspondence: \_\_\_\_\_

Tele/Fax & Mobile No. \_\_\_\_\_

Email: \_\_\_\_\_

Demand Draft No: \_\_\_\_\_ drawn on

\_\_\_\_\_ dated \_\_\_\_\_

Payable at Surat

**Date:** \_\_\_\_\_ **Signature of Applicant**

The candidate

Mr/Mrs/Dr/Prof \_\_\_\_\_

is hereby officially permitted by this institute to attend the STTP titled “Deep Learning Applications” (DLA-2019) during the period 18<sup>th</sup> June – 22<sup>nd</sup> June, 2019 at SVNIT Surat.

DIRECTOR / PRINCIPAL with SEAL

TEQIP-III Sponsored

**One-Week  
Short Term Training Programme  
(STTP)**

On

**Deep Learning Applications  
(DLA-2019)**

18<sup>th</sup> June – 22<sup>nd</sup> June, 2019

**Coordinator**

Dr. Krupa N. Jariwala

**Co-coordinator**

Dr. Divakar Yadav

(MMMUT, Gorakhpur, U.P.)



Organized By

**Computer Engineering Department  
Sardar Vallabhbhai National Institute  
of Technology (SVNIT)  
Ichhanath, Surat-395007, Gujarat, India**

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Email: [dlasttp2019@gmail.com](mailto:dlasttp2019@gmail.com)

## ABOUT THE INSTITUTE AND SURAT

The institute, one of the pioneering engineering institutions of the country, was established in 1961 as Sardar Vallabhbhai Regional College of Engineering & Technology and was given a status of National Institute of Technology, in 2002. At present, there are six undergraduate courses, seventeen postgraduate courses and Ph.D. programmes in all disciplines of engineering and applied sciences. It has an excellent placement record with a number of top ranking companies visiting the campus. The whole campus has connectivity of the internet with fiber optic network including faculty quarters and student hostels.

The institute is located at Surat, about 260 kms North of Mumbai and is very well connected by rail and road links to Mumbai as well as Ahmedabad (250 kms)/Vadodara (150 kms). The institute is approximately 10 kms away from Surat Railway Station and 10 kms from the Surat Airport (STV). Surat is the industrial city with historical importance and is well known for Textile, Jari and Diamond industries. The leading industries like RIL, ONGC, Kribhco, L&T, ESSAR, NTPC, and GAIL are established in Surat- Hazira area.

## ABOUT THE DEPARTMENT

The department is running UG programme B.Tech. Computer Engineering since 1989 and PG programme M.Tech. Computer Engineering since 2006. Department is also offering Ph.D. programme since 2005. The department has well qualified and dedicated faculty members with the specialization in niche areas like information security, Cloud computing, operating systems, image processing, Data Mining, wireless sensor network, computer vision etc.

## PATRON

Prof. S. R. Gandhi  
Director, SVNIT

## COORDINATOR

Dr. Krupa. N. Jariwala

## CO-COORDINATOR

Dr. Divakar Yadav  
(MMMUT, Gorakhpur, U.P.)

## DEPARTMENT HEAD

Prof. M. A. Zaveri

## LOCAL TECHNICAL COMMITTEE

Prof. M. A. Zaveri  
Prof. R. P. Gohil  
Prof. R. G. Mehta  
Dr. K. N. Jariwala  
Dr. D. P. Rana  
Dr. S. J. Patel  
Dr. U. P. Rao  
Mr. Gaurav Buddhawar  
Mrs. Rasika Khade

## OFFICIAL CORRESPONDENCE ADDRESS

**The Coordinator,**  
TEQIP-III Sponsored STTP on Deep Learning Applications (DLA-2019), Computer Engineering Department, S. V. National Institute of Technology, Ichchhanath, Surat – 395 007, Gujarat, India.  
**Contact:** 8830582671  
**Email:** [dlasttp2019@gmail.com](mailto:dlasttp2019@gmail.com)

## ELIGIBILITY

Students/Faculty Members from academic institutes, universities, research scholars and industry personnel working in the concerned/allied discipline.

## IMPORTANT DATES

Last date for Registration: 5<sup>th</sup> June, 2019  
List of Eligible Candidates: 6<sup>th</sup> June, 2019  
(mail will be sent to eligible candidates)

## REGISTRATION FEES

Students & Research Scholars	: Rs. 1000/- only
Faculty Members	: Rs. 2000/- only
Industry Participants	: Rs. 3000/- only

Link for online registration:

<https://forms.gle/tbxskMvDTFEbwomFA>

After online registration, It is **necessary** to send the Registration Form and DD (payable at Surat) drawn in the favour of “Director, SVNIT-TEQIP IRG”, to the official address for correspondence on or before 10<sup>th</sup> June, 2019.

## TRAVEL

The participants have to bear their own travelling expenses, if any. Limited number of paid accommodation is available for out station participants on twin sharing basis (First Come First Serve). A course-kit and working lunch will be served during the period of the STTP.