**Walk in Interview for the posts of Teaching Assistants**

**(Purely on contractual basis for 11 months)**

The institute is going to conduct walk-in-interview offline mode (written test and interview) for the post of Teaching Assistants (purely on contract basis for 11 months) in Department of Electrical Engineering on **1st July 2025** (Reference: No. Dean/Acad/TA/45/2024-25 dated 30/04/2025).

The minimum qualifications and remuneration to be paid to the Teaching Assistant(s) purely on contract basis is as given below:

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| **Sr.**  **No.** | **Category** | **Monthly remuneration** |
| 1 | 1st Class or an equivalent CGPA in M. Tech / M.E. in Electrical Engineering with specialization in relevant field *and* 1st Class or an equivalent CGPA in B. Tech / B.E. in Electrical Engineering with Good Academic Record. | Rs. 45,000/- |
| 2 | Ph.D. (in appropriate field) with 1st Class or an equivalent CGPA in B. Tech / B.E in Electrical Engineering *and* 1st Class or an equivalent CGPA in M. Tech / M.E. in Electrical Engineering with specialization in relevant field with Good Academic Record. | Rs. 60,000/- |

* **The mail for the confirmation of your presence for the walk-in interview is to be sent to hod@eed.svnit.ac.in**
* **The prescribed application form is available on the institute website. (**[**Link of Application form**](file:///C:\Users\DELL\Downloads\TA-Application%20Form.pdf)**)**

**The tentative schedule for walk-in-interview is as follows:**

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| **Walk-In-Interview Process** | **Date: 01st July 2025, Tuesday** | **Venue: Department of Electrical Engineering** |
| Registration | 9:30 am to 10:30 am | Seminar Hall DoEE Building |
| Document Verification | 10:30 am to 12:00 pm |
| Written Test | 12:30 pm to 1:30 pm |
| Interview | 2:30 pm onwards |

**The candidate is requested to remain present with filled application in the prescribed form only along with originals supporting documents such as degree certificates, semester - wise marks sheets, experience certificates, testimonials, etc. and one set of *self-attested* copy of the same documents at 9:30 am in the Department of Electrical Engineering on 01st July 2025 (Tuesday).**

**Any wrong or misleading information supplied by the candidate may lead to summarily rejection of the application/appointment, if found subsequently.**

**Applications on plain paper or incomplete applications will not be accepted.**