

# DEPARTMENT OF ELECTRONICS ENGINEERING

## **Walk-in-Interview for the posts of Teaching Assistants** **(purely on ad-hoc basis for 11 months)**

**Date: 30<sup>th</sup> June, 2025**

The institute is going to conduct a walk-in-interview in offline mode (written test and interview) for the post of Teaching Assistant (purely on an ad-hoc basis for 11 months) in the Department of Electronics Engineering on **30<sup>th</sup> June, 2025**.

The minimum qualifications and remuneration to be paid to the Teaching Assistant(s) purely on ad-hoc basis is as given below:

Sr. No.	Category	Monthly remuneration
1	1 <sup>st</sup> Class or an equivalent CGPA (Min 6.5 on the scale of 10) in M. Tech / M.E. and 1 <sup>st</sup> Class or an equivalent CGPA (Min 6.5 on the scale of 10) in B. Tech / B.E with Good Academic Record in the branch of Electronics Engineering	Rs.45,000/-
2	Ph. D. (in appropriate branch) with 1 <sup>st</sup> Class or an equivalent CGPA (Min 6.5 on the scale of 10) in B. Tech / B.E and 1 <sup>st</sup> Class or an equivalent CGPA (Min 6.5 on the scale of 10) in M. Tech / M.E.(Engg.) with Good Academic Record in the branch of Electronics Engineering	Rs.60,000/-

**The prescribed application form is available on the institute website.**

**The schedule for walk-in-interview is as follows:**

Walk-In-Interview Process	Date: 30 <sup>th</sup> June, 2025	Venue: Department of Electronics Engineering
Registration and Document Verification	9:00 AM to 10:30 AM	EC004N
Written Test (if required)	11:00 AM to 12:00 NOON	EC022N
Interview	2:00 PM onwards	EC004N

- **The candidates are informed to send a soft copy of the complete application in a single PDF to [office@eced.svnit.ac.in](mailto:office@eced.svnit.ac.in) on or before 20/06/2025, 5:00 PM.**
- The candidate is requested to remain present with filled application in the prescribed form only along with originals supporting documents such as degree certificates, semester- wise marks sheets, experience certificates, testimonials, etc. and one set of self-attested copy of the same documents at 8:30 am in the Department of Electronics Engineering on 30<sup>th</sup> June, 2025.
- Any wrong or misleading information supplied by the candidate may lead to summarily rejection of the application/appointment, if found subsequently.
- Applications on plain paper or incomplete applications will not be accepted.